2nd semester lectures the 4th class

Dr. Ahmed Al-Ghaban

Thin Film Deposition

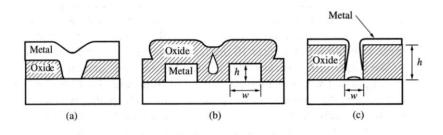
Topics:

•Chemical Vapor Deposition

- Physical Vapor Deposition
 - Evaporation
 - Sputtering
- •Strengths and Weaknesses
- Basic Calculations

Issues related to thin film deposition

- Quality:
 - Composition
 - Defect density (e.g. pinholes)
 - Contamination
 - Mechanical and electrical properties
 - Good adhesion
 - Minimum stress
- Topography
 - Uniform thickness on non-planar surfaces
 - Step coverage
 - Conformal coverage: uniform
 - Space filling in holes, channels
 - Voids

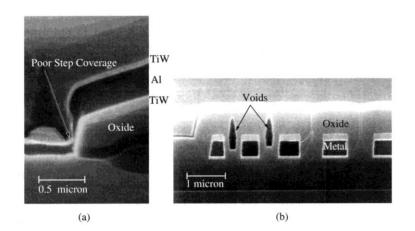


Thin film filling issues:

(a) shows good metal filling of a via or contact hole in a dielectric layer

(b) silicon dioxide dielectric filling the space between metal lines, with poor filling leading to void formation

(c) poor filling of the bottom of a via hole with barrier or metal



SEM photo showing typical coverage and filling problems

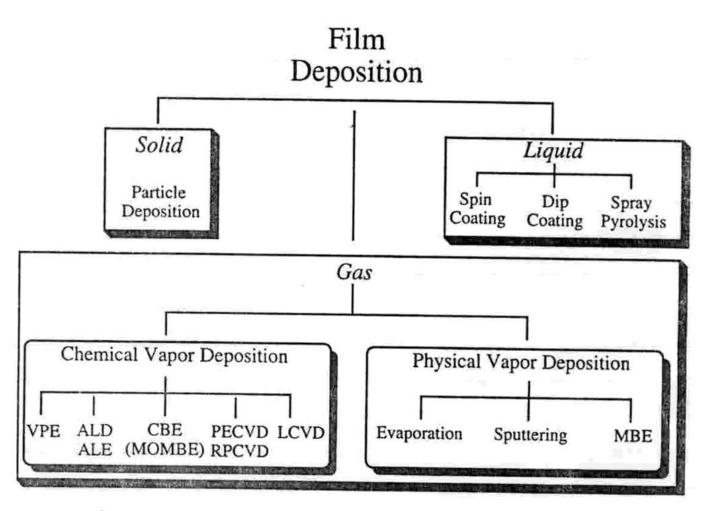
Classification of Thin Films

(a) single crystals
(b) epitaxial
(c) polycrystalline
(d) amorphous.

(a) high surface to volume ratio
(b) geometrical control (dictated by substrate)
(c) compact
(d) single crystal like properties

Uses of Thin Films

(a) microelectronic devices (b) telecommunication devices (c) wear resistant coatings (d) decorative coatings (e) optical coatings (windows, solar cells, etc.) (f) sensors (g) catalysts



Scheme 5.1 Relationship of different film deposition methods.

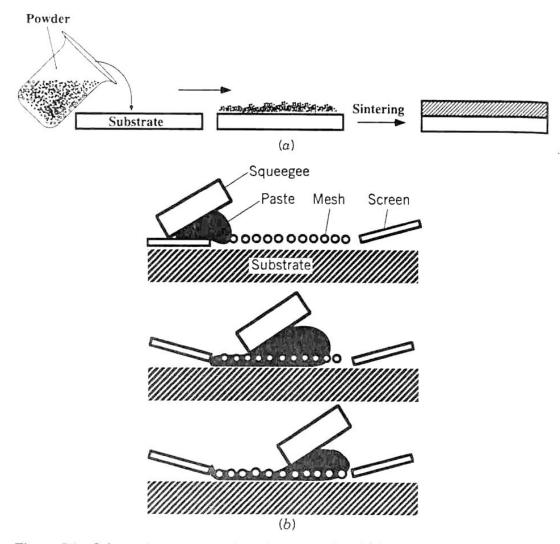


Figure 5.2 Schematic representation of an example of (a) solid-state deposition and (b) screen printing.

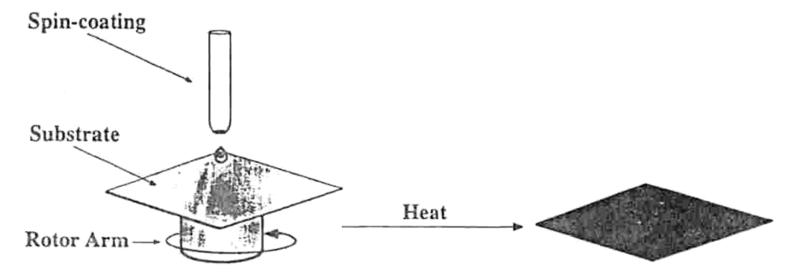
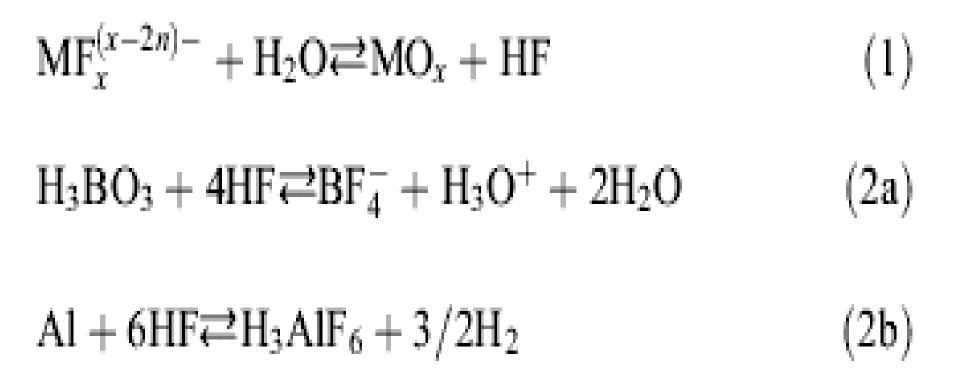
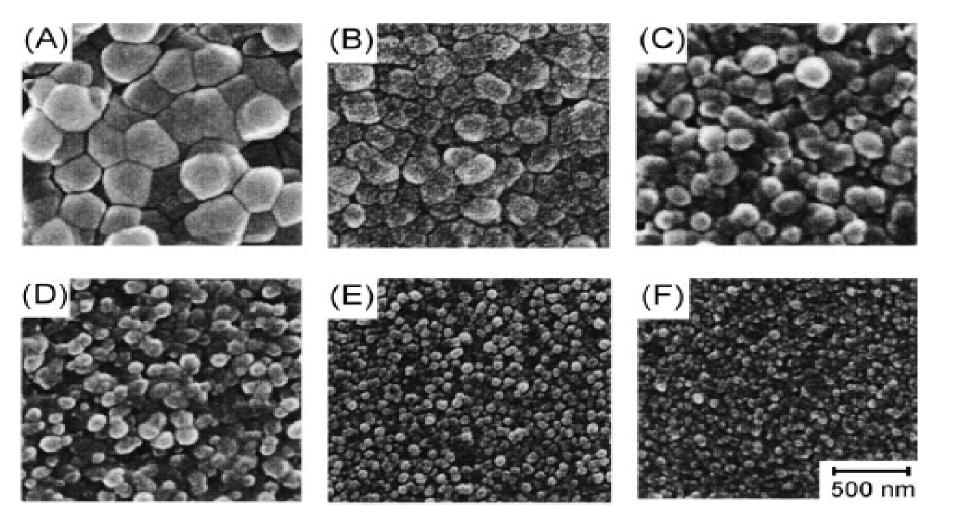


Figure 5.3 Schematic representation of liquid-phase deposition by spin coating.

liquid phase deposition process, LPD)



SEM images of the deposited h-FeOOH films at various concentrations. Concentration of H3BO3: (A) 0.30, (B) 0.40, (C) 0.45, (D) 0.50, (E) 0.55, and (F) 0.60 mol dm⁻³.



- LPD-grown silica has been widely tested for application in integrated circuit processing,
- and in metal-oxide-semiconductor (MOS)
- and in complementary metal-oxidesemiconductor (CMOS)
- LPD silica is being explored for use in waveguides
- and in MOS solar cells

Chemical bath deposition (CBD)

1) complexant-H₂O equilibrium:

$$2L^{k-} + 2H_2O \Leftrightarrow 2LH^{-k+1} + 2OH^- \tag{1}$$

2) dissociation of water:

 $nH_2O \Leftrightarrow nOH^- + nH^+$ (2)

3) displacement of ligands:

 $nOH^- + M(L)_i^{(n-ik)+} \rightarrow M(OH)_n(s) + iL^{k-}$ (3)

deprotonation to form oxide:

 $M(OH)_n(s) \rightarrow MO_{n/2}(s) + n/2H_2O \tag{4}$

Some Principles involved

- while the water itself provides oxygen, initially in the form of OH⁻ ions.
- Hydrolysis can occur even in acidic solutions when the metal cation is easily hydrolyzable, as with Fe³⁺, Ti⁴⁺, Zr⁴⁺, and Al³⁺
- CBD requires supersaturated solutions, i.e. the concentrations must exceed the solubility product
- In a closed system, growth cannot continue once the reactants have been depleted below this point, which determines the thickness of the film

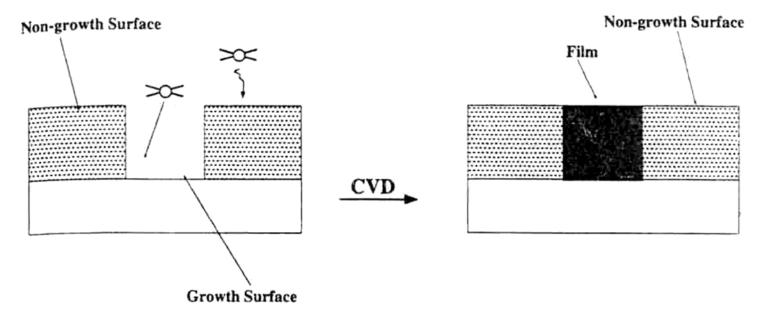


Figure 5.6 Schematic representation of selective deposition.

Chemical Vapor Deposition

- Involves one or more gas phase species which react on a solid surface (substrate) to deposit a solid flim.
- Typically, the reaction is initiated by heating the substrate. Other mechanisms of supplying the activation energy necessary to initiate reactions include: laser CVD, photo CVD, and plasma enhanced CVD.

- Typical Parameters
- Pressure 0.1 torr 1 atm
 Substrate Temp. 100° C 1500° C
 Deposition Rate 60Å/min 300,000Å/min
- The use of organometallic precursors as gas phase species (MOCVD) can result in significant reduction of the substrate temperature.
- Examples

WCl6 (g) + 3H2 (g) \rightarrow W (s) + 6HCl (g) SiH4 (g) + O2 (g) \rightarrow SiO2 (s) + 2H2 (g) 6TiCl4 (g) + 8NH3 (g) \rightarrow 6TiN (s) + 24HCl (g) + N2 (g)

Advantages

- High deposition rates
- Capable of accurate stoichiometry control
- High quality films (low defect concentration)
- No need for high vacuum components
- Favored in industrial settings

Disadvantages

- High operating temperatures
- Toxic Gases
- Generally applicable to binary and elemental films
- Not suitable for production of metastable products

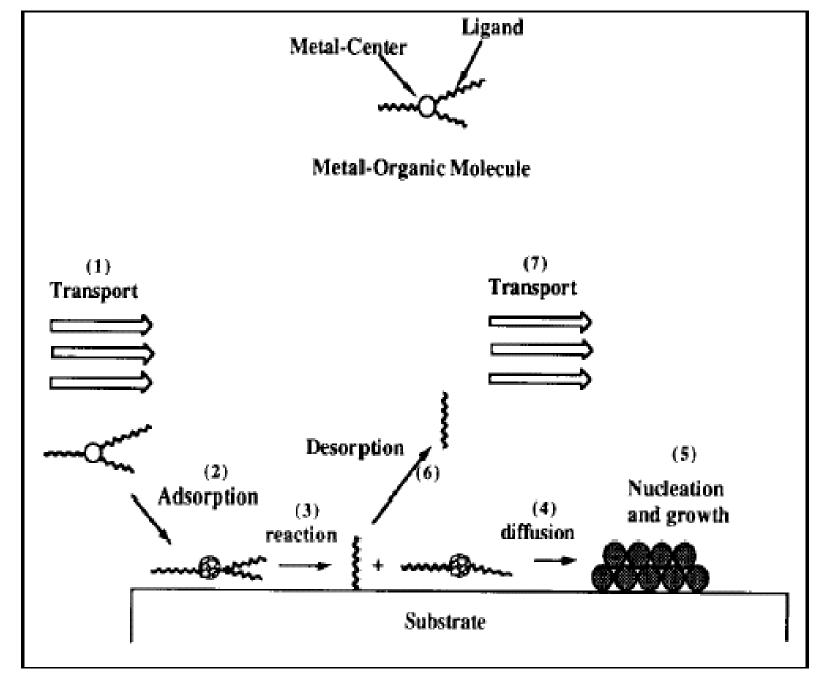


Fig. 2: An illustration of the process occurring in OMCVD.

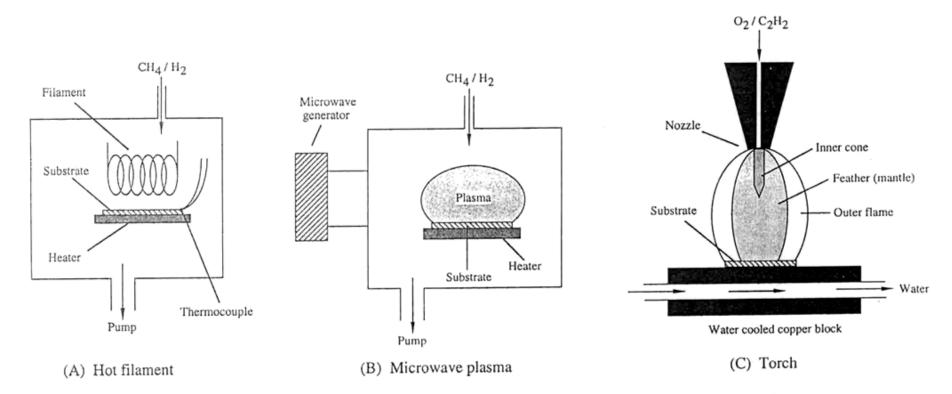


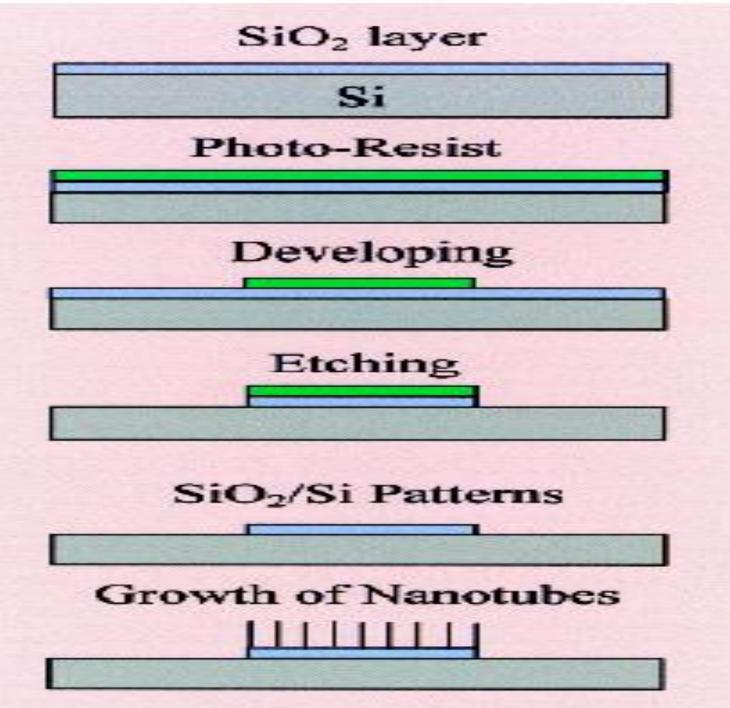
Figure 5.20 Schematic diagrams of three of the most commonly used types of diamond CVD apparatus: (a) hot filament reactor; (b) microwave-plasma-enhanced CVD reactor; (c) oxyacetylene torch.

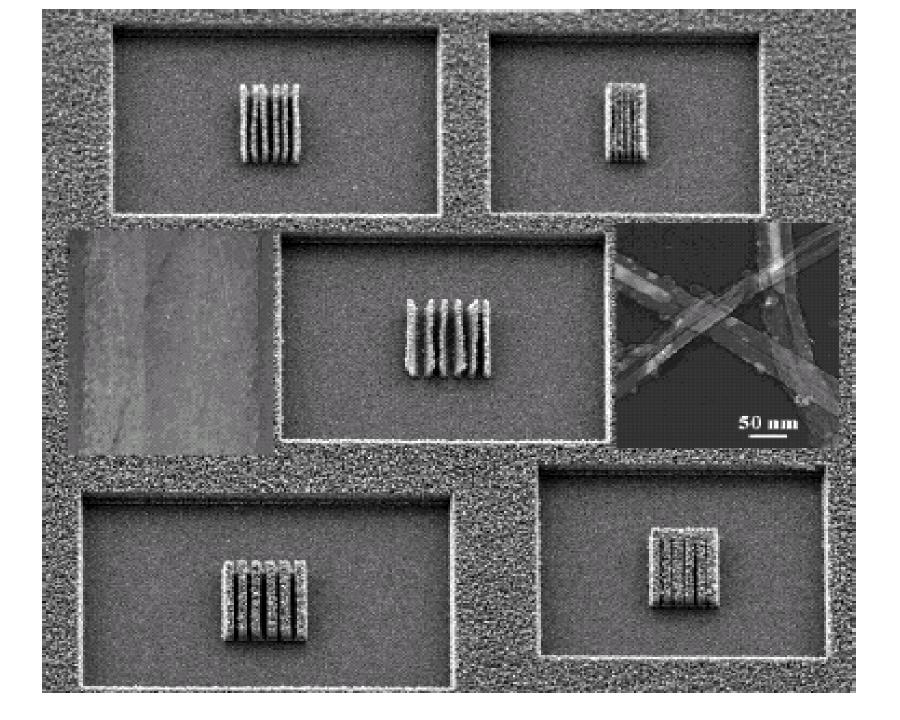
Highly Organized Carbon Nanotube by Chemical Vapor Deposition

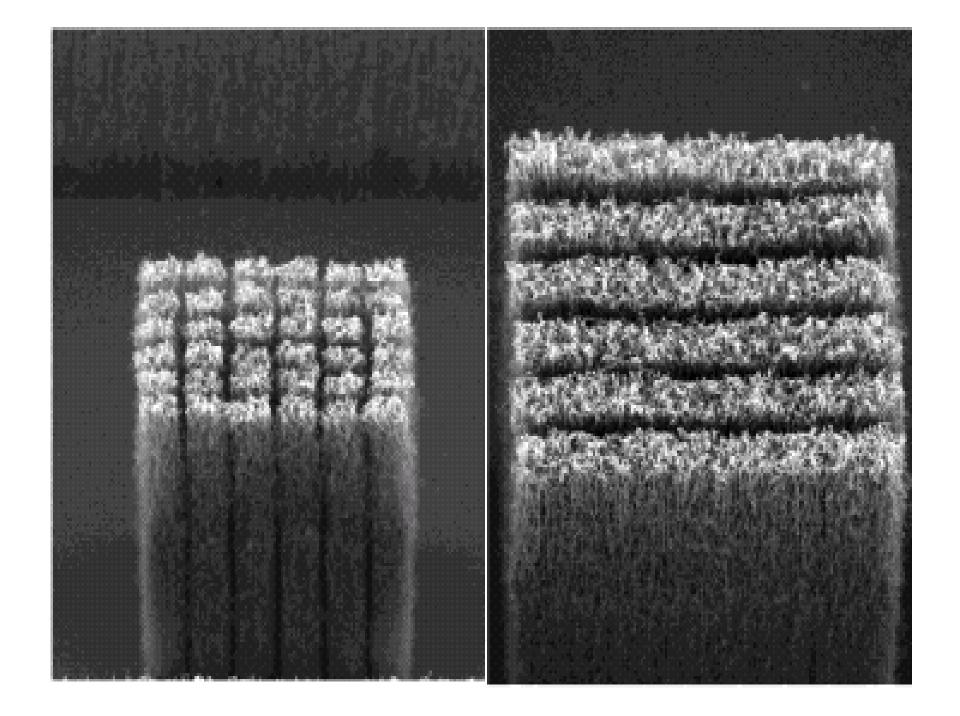
B. Q. Wei, R. Vajtai, Y. Jung, J. Ward, R. Zhang, G. Ramanath, P. M. Ajayan

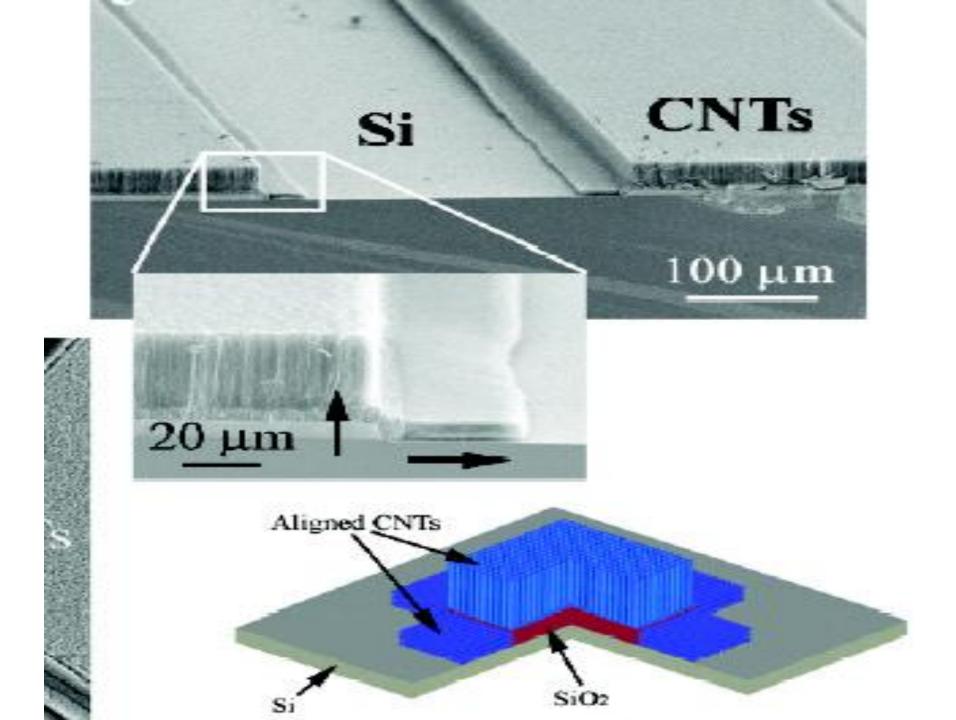
xylene + $[Fe(C_5H_5)_2]$

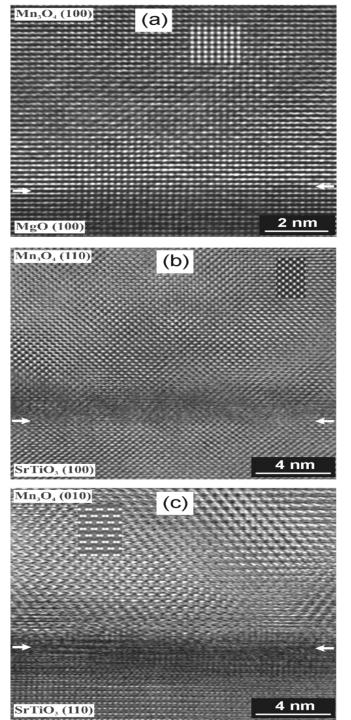
with flowing argon to 100mTorr and heated gradually to 800 °C





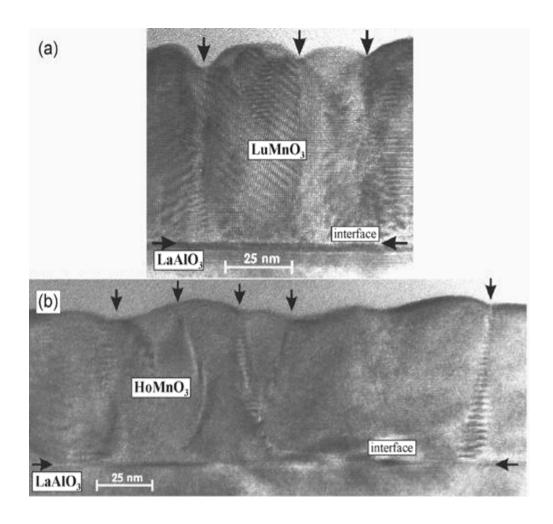






Examples of Epitaxy film

Epitaxial stabilization of oxides in thin films Gorbenko OY, Samoilenkov SV, Graboy IE, Kaul AR CHEMISTRY OF MATERIALS 14 (10): 4026-4043 OCT 2002

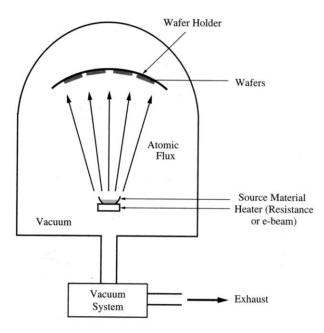


Two types of thin film deposition: CVD and PVD

- CVD
 - Reactive gases interact with substrate
 - Used to deposit Si and dielectrics
 - Good film quality
 - Good step coverage
- PVD
 - Used to deposit metals
 - High purity
 - Line of sight

Physical Vapor Deposition: PVD

2 types: *evaporation* and *sputtering*



Advantages:

Versatile – deposits almost any material

Very few chemical reactions

Little wafer damage

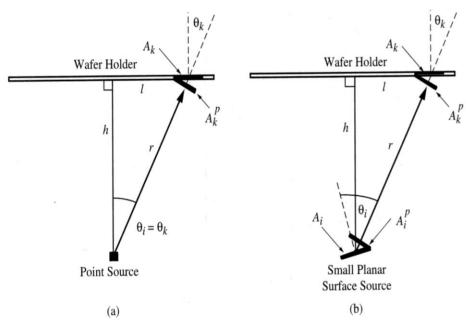
Limitations:

Line-of-sight

Shadowing

Thickness uniformity

Difficult to evaporate materials with low vapor pressures



Flux from a point source

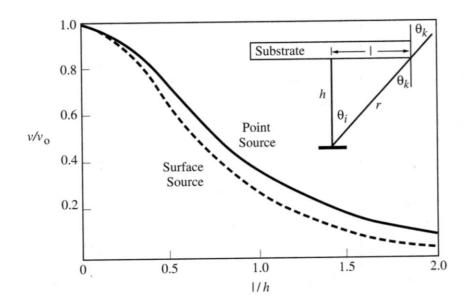
$$F_k = \frac{R_{evap}}{\Omega r^2} \cos \theta_k$$

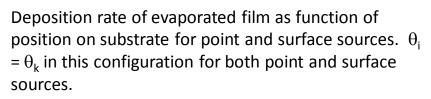
Deposition rate from a surface source:

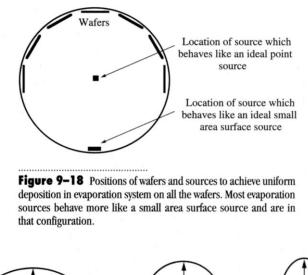
$$v = \frac{R_{evap}}{\Omega N r^2} \cos \theta_k \cos \theta_i$$

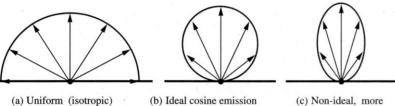
Geometries of flux and deposition of small areas on a flat wafer holder for (a) a point source and (b) a small planar surface source

$$R_{evap} = 5.83 \times 10^{-2} A_s \left(\frac{m}{T}\right)^{1/2} P_e$$









from a small planar

 $(n = 1 \text{ in } \cos^n \theta)$

distribution)

surface source.

(c) Non-ideal, more anisotropic emission from a small planar surface source.

> $(n > 1 \text{ in } \cos^n \theta$ distribution)

Figure 9–19 Emitted fluxes for point sources and small surface areas sources.

emission from a

point source

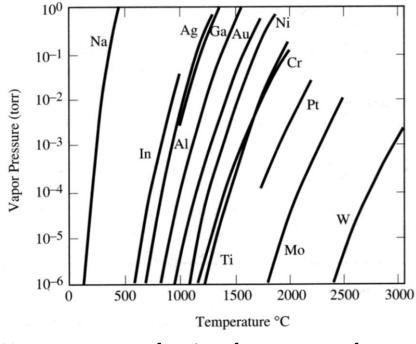
Mean free path λ :

$$\lambda = \frac{kT}{\sqrt{2}\pi d^2 P_e}$$

k = 1.36 x 10⁻² erg/at-K

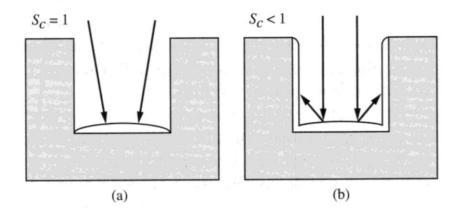
d≈.4 x 10⁻⁸ cm

 P_e = partial pressure (torr)



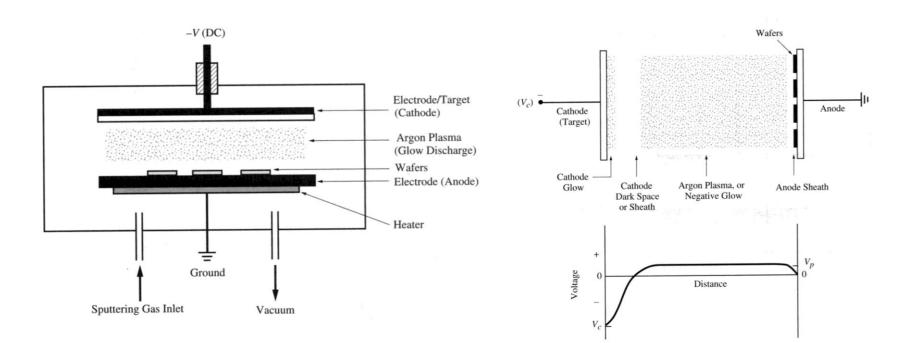
Vapor pressure as a function of temperature of commonly evaporated metals

Sticking coefficient: $S_c = F_{reacted}/F_{incident}$



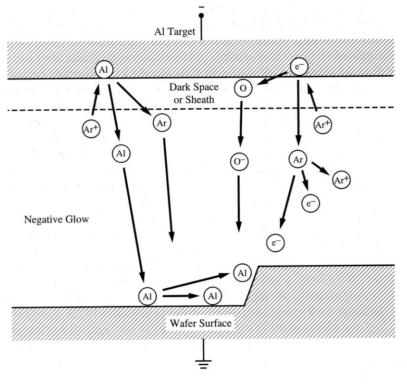
The depositing species have a high sticking coefficient (close to 1) in (a), so that they are deposited where they first strike. In (b) the depositing species have a low sticking coefficient (<<1) so that man are reemitted and deposit elsewhere on the topography, such as the sidewalls.

Sputtering

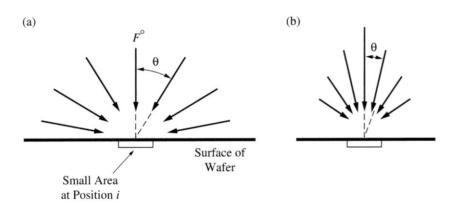


Schematic diagram of DC-powered sputter deposition equipment

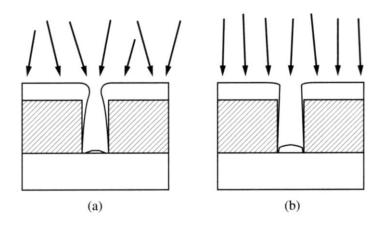
Plasma structure and voltage distribution in DC sputter system



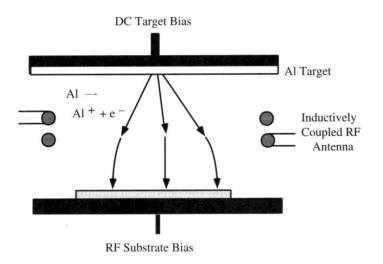
Processes in sputter deposition



Distribution of arrival fluxes for (a) uniform or isotropic arrival distribution and (b) directed or anisotropic arrival distribution. Arrival angle distribution ($\cos^n\theta$) is defined by arrival flux relative to unit surface area. This flux is equal to the normal component of incoming flux, relative to the vertical direction for a horizontal surface.

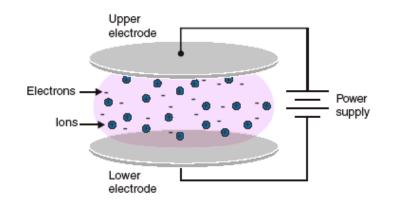


Effect of arrival angle distribution of depositing species on filling trenches or holes. In (a) a relatively wide arrival angle distribution leads to poor bottom filling or coverage, while (b) a narrower arrival angle distribution leads to better bottom filling. The higher the aspect ratio of the feature, the narrower the arrival angle distribution must be for adequate coverage.



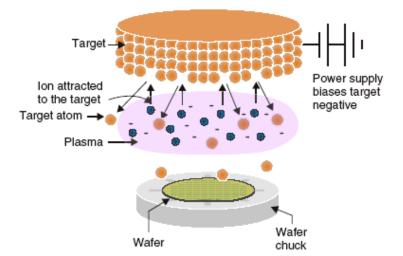
Schematic diagram of ionized sputter deposition system (ionized PVD) showing atomic flux lines

Sputter Deposition



Plasma - a low pressure gas where a high energy field is used to drive ionization creating a large number of ions and free electrons.

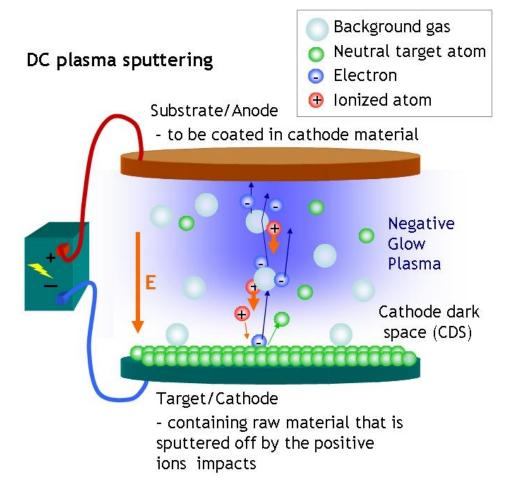
Sputter deposition - ions from a plasma are attracted towards a target made of the material to be deposited, the ions strike the target physically knocking target atoms loose, the target atoms then land on the wafer.



DC Sputter Deposition

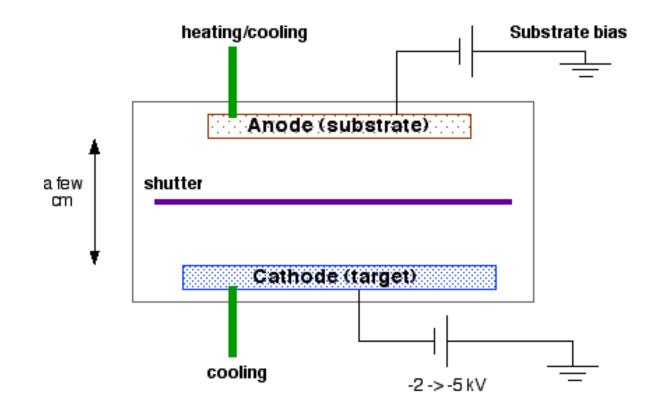
Sputter deposition is a physical vapor

deposition (PVD) method of depositing thin films by sputtering, that is ejecting, material from a "target," source, which then deposits onto a "substrate," such as a silicon wafer. Sputtered atoms ejected from the target have a wide energy distribution, typically up to tens of eV (100,000 K). The sputtered ions (typically only a small fraction (~1% of the ejected particles are ionized) can ballistically fly from the target in straight lines and impact energetically on the substrates or vacuum chamber (causing resputtering). Energetic ions sputter material off the target which diffuse through the plasma towards the substrate where it is deposited. There is no strong plasma glow around the cathode since it takes a certain distance for the plasma to be generated by electron avalanches started by a few secondary electrons from the sputtering process.



http://en.wikibooks.org/wiki/Microtechnology/Additive_Processes

DC Sputtering

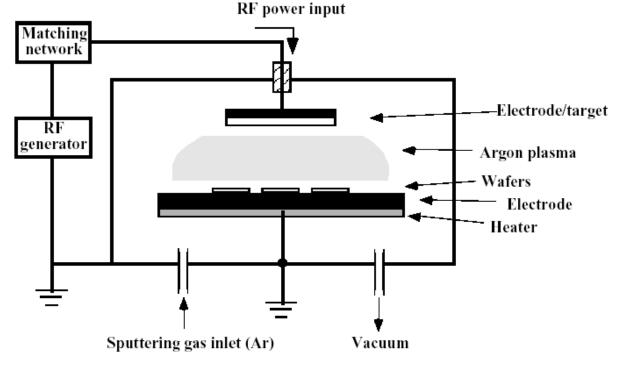


DC Sputtering Schematic

RF Sputter Deposition

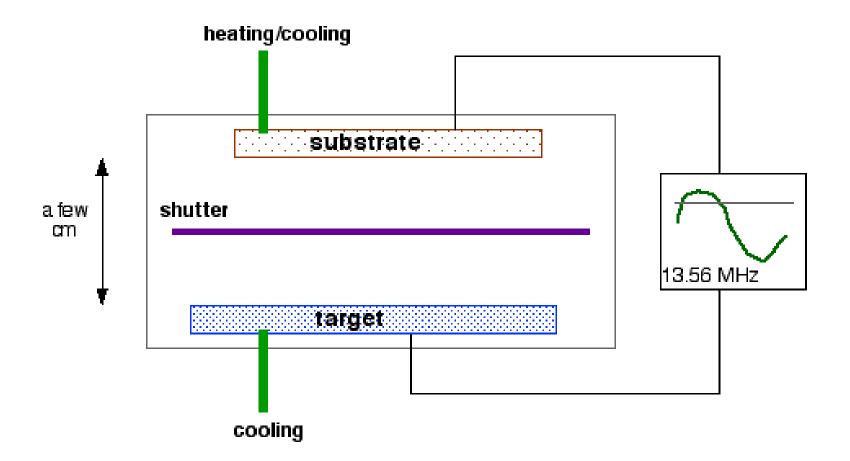
RF Sputter Deposition

- For DC sputtering, target electrode is conducting.
- To sputter dielectric materials use RF power source.



RF Sputter Deposition Schematic

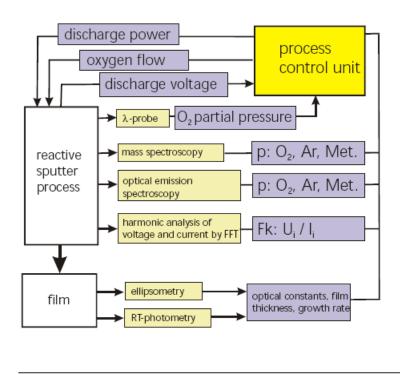
RF Sputter Deposition



RF Sputtering Schematic

Reactive Sputtering

Process control for reactive sputtering



Manipulated variable:

discharge power, reactive gas flow

Controlled variable:

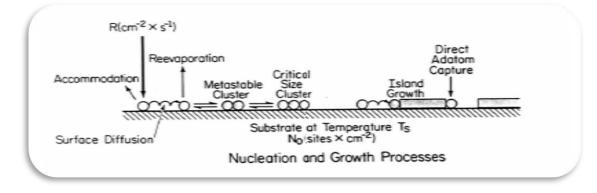
optical constants, partial pressure, voltage, harmonic analysis (specific) deposition rate

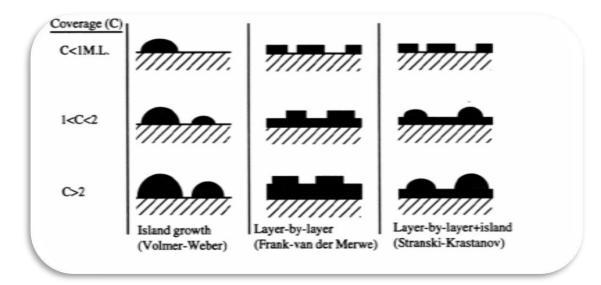
Process control unit:

PID controller, fuzzy controller, expert system

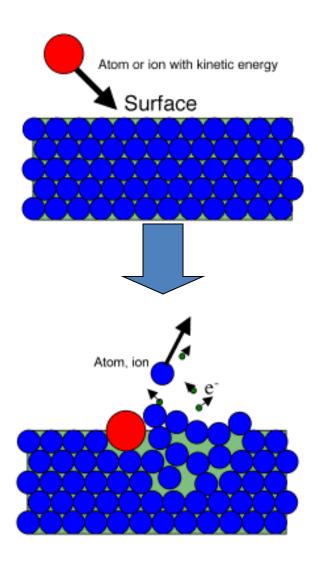
 Continuous adjustment of particle fluxes for all materials

Elements of Thin Film Growth





Thin Film	Equipment	Typical Reactions	Comments
Epitaxial silicon	APCVD, LPCVD	$SiH_4 \rightarrow Si + 2H_2$	1000–1250°C.
		$\begin{array}{l} SiCl_4 + 2H_2 \rightarrow Si + 4HCl \\ Also SiHCl_3, SiH_2Cl_2 \end{array}$	Reduce pressure for lower- temperature deposition.
Polysilicon	LPCVD	Same as epitaxial Si	575–650°C. Grain structure depends or deposition conditions and doping.
Si ₃ N ₄	LPCVD, PECVD	$\begin{array}{l} 3SiH_4 + NH_4 \rightarrow \\ Si_3N_4 + 12H_2 \end{array}$	650–800°C for oxidation mask. 200–400°C (PECVD) for passivation.
SiO ₂	LPCVD, PECVD, HDPCVD, APCVD	$\begin{array}{l} SiH_4 + O_2 \rightarrow SiO_2 + 2H_2\\ Si(OC_2H_5)_4 \ (+O_3)\\ \rightarrow SiO_2 \ + \ byproducts \end{array}$	200–800°C. 200–500°C (LTO)—may require high T anneal.
			25–400°C (TEOS-ozone, PECVD, HDPCVD).
Al construction of the second	Magnetron sputter deposition		25–300°C (standard deposition). 440–550°C (hot Al for in situ reflow). CVD difficult for alloys (Al-Cu-Si).
Ti and Ti-W	Magnetron sputter deposition (standard, ionized, or collimated)		CVD difficult. Nitrogen can be added to Ti-W to stuff grain boundaries.
W	LPCVD	$\begin{array}{l} 2WF_6 + 3SiH_4 \rightarrow \\ 2W + 3SiF_4 + 6H_2 \end{array}$	250–500°C. Blanket deposition with
		$WF_6 + 3H_2 \rightarrow W + 6HF$	two-step process using both reactions is common.
TiSi ₂	Sputter and surface reaction	$\begin{array}{l} Ti(sputtered) +\\ Si(exposed) \rightarrow TiSi_2 \end{array}$	Sputter/reaction gives self-aligned silicide.
	Cosputtering or CVD		
TiN	Reactive sputter deposition	$Ti + N_2 (in plasma) \rightarrow TiN$	Organometallic source possible for MOCVD deposition.
	CVD	$\begin{array}{l} 6\text{TiCl}_4 + 8\text{NH}_3 \rightarrow \\ 6\text{TiN} + 24\text{HCl} + \text{N}_2 \end{array}$	TiN can also be formed in TiSi ₂ process.
Cu	Electroplating, electroless, sputtering, CVD	$Cu^{2+} + 2e^- \rightarrow Cu$	Electroplating is most common method today.



Microscopic View of

The impact of **Sputtering** Surface produces sputtering from the surface as a result of the momentum transfer from the incoming particle. Unlike many other vapor phase techniques there is no melting of the material.

History of Sputtering

- The verb to SPUTTER originates from Latin SPUTARE(To emit saliva with noise).
- Phenomenon first described 150 years ago ...
 Grove (1852) and plücker (1858) first reported vaporization and film formation of metal films by sputtering.
- Key for understanding discovery of electrons and positive ions in low pressure gas discharges and atom structure (J.J. Thomson, Rutherford), 1897--
- Other names for SPUTTERING were SPLUTTERING and CATHODE DESINTEGRATION.

ref: www.gencoa.com

Target Erosion



- Target erosion is greatest where the magnetic field and the subsequent plasma density is greatest.
- This leads to inefficient use of target material, particularly in the case of ferromagnetic targets.

Magnetron Guns



ref: www.lesker.com

The Latest in UHV Sputtering

http://www.ajaint.com



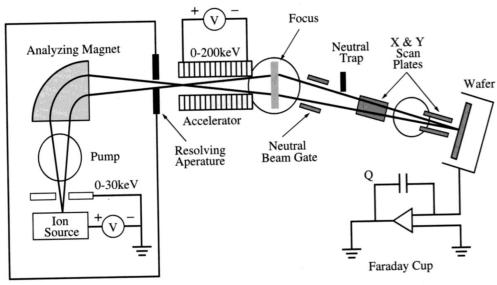
- A UHV, magnetron sputter source that fits through the port of a 2.75" CF flange complete with its tilt gimbals assembly.
- The AJA International new A310-XP only needs a 2.75" CF to accommodate the source head, tilt gimbals and gas injection/isolation chimney.
- This revolutionary new design is true UHV all ceramic to metal construction.

Ion Implantation

Topics:

- Deposition methods
- Implant
 - -Depth & Distribution
 - -Masking effects
- Damage annealing

Manufacturing Methods



Schematic of an ion implanter

Ion source: Vaporized solids or gases – e.g., $-B = \alpha I$

- Arsine

- Vapors form various species, suphus
- as B⁺⁺, B⁺, BF⁺, BF₂⁺, along with $\sqrt{m} = q/\sqrt{2E(\alpha R)I}$ - Phosphine - BF2

These gases are **extremely toxic!** Mixed in 15% concentrations with H2

Applying a magnetic induction, **B**, to speeding atoms causes them to bend around the curve of radius R:

 $\frac{1}{2}$ mv² = q·**v** x **B**

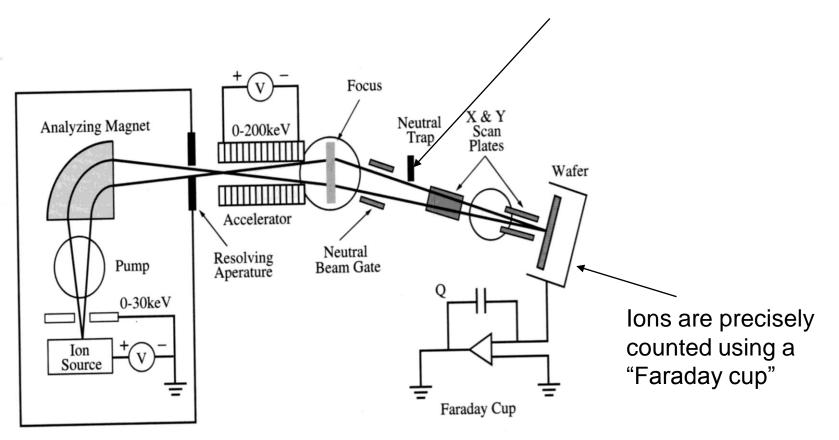
Velocity can be expressed as:

$$\mathbf{v} = \sqrt{(2E/m)} = \sqrt{(2qV_{ext}/m)}$$

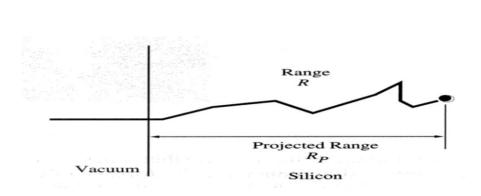
B can be expressed as a function of current:

By adjusting current, I, ions can be separated by mass

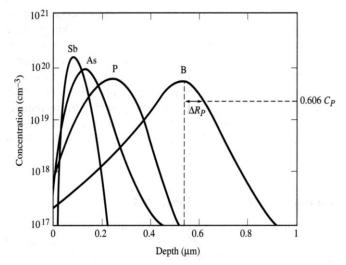
Neutrals are separated by the electrostatic deviation of the focus beam



Ion Path and Distribution



Schematic of the actual range of an implanted ion and the projected range normal to the surface.

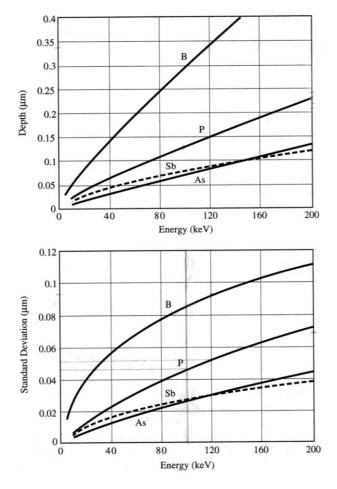


Distribution of ions implanted into crystalline silicon at an energy of 200 keV. Light ions penetrate more deeply, and have more asymmetric distribution than heavy ions.

Definitions: C_p = peak concentration

- $\mathbf{R}_{\mathbf{p}}$ = average projected depth
- $\Delta \mathbf{R}_{\mathbf{p}} = \text{Standard Deviation} =$

$$\sqrt{\frac{1}{N-1}\sum_{i=1}^{N}(x_i - \bar{x})^2}$$



Plots of Rp and DRp for common dopants in crystalline Si tilted and rotated to simulate random direction.

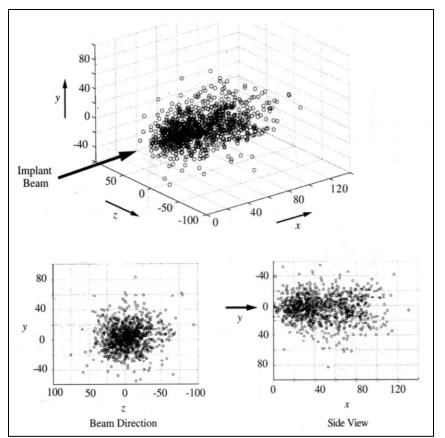
Dopant concentration curves are modeled using Gaussian distribution:

$$C(x) = C_p \exp\left(-\frac{\left(x - R_p\right)^2}{2\Delta R_p}\right)$$

Total number of ions implanted:

$$Q = \int_{-\infty}^{\infty} C(x) dx = \sqrt{2\pi} \Delta R_p C_p$$

This relates the dose to the peak concentration

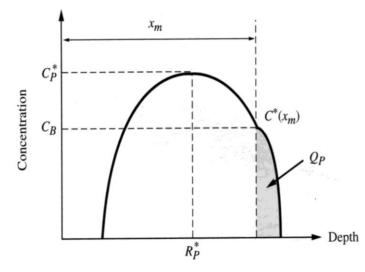


Simulated 3D distribution of 1000 P atoms implanted at 35 keV (top). Axis units are nm.

Lateral distributions are also modeled using a Gaussian:

$$C(x, y) = C_{vert}(x) \exp\left(-\frac{y^2}{2\Delta R_{pl}^2}\right)$$

Masking Layers



Solving for *x_m*:

$$C^{*}(x_{m}) = C_{p}^{*} \exp\left(-\frac{(x_{m} - R_{p}^{*})^{2}}{2\Delta R_{p}^{*^{2}}}\right) \le C_{B}$$

$$x_m = R_p^* + \Delta R_p^* \sqrt{2 \ln\left(\frac{C_p^*}{C_B}\right)} = R_p^* + m\Delta R_p^*$$

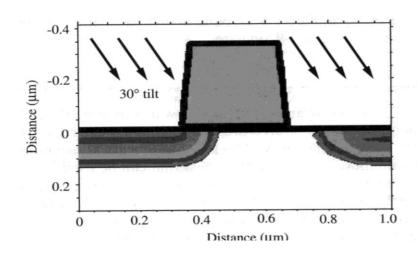
Schematic of masking process, where dose Q_p penetrates the mask of thickness x_m . * denotes mask

$$Q_{p} = \frac{Q}{\sqrt{2\pi\Delta R_{p}^{*}}} \int_{x_{m}}^{\infty} \exp\left[\frac{x - R_{p}^{*}}{\sqrt{2\Delta R_{p}^{*}}}\right]^{2} dx$$

The dose that penetrates is:

$$Q_{p} = \frac{Q}{2} \operatorname{erfc}\left[\frac{x_{m} - R_{p}^{*}}{\sqrt{2}\Delta R_{p}^{*}}\right]$$

Implantation not Perpendicular to Mask



The dopant distribution resulting from tilt implantation followed by diffusion is given by combining implantation concentration and diffusion equations:

Implantation

$$C(x) = C_p \exp\left[-\frac{\left(x_m - R_p\right)^2}{2\Delta R_p^2}\right]$$

Simulation of 50 keV P implant at a tilt angle of 30°, showing asymmetrical implant distribution and shadowing caused by the gate polysilicon

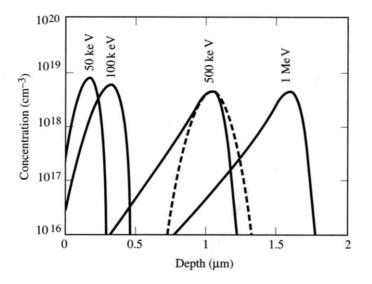
Diffusion

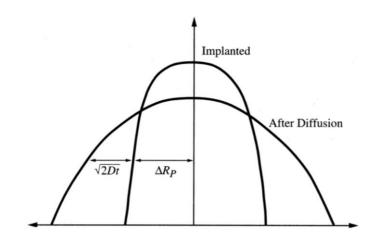
$$C(x) = C(0,t) \exp\left(-\frac{x^2}{4Dt}\right)$$

$$C(0,t) = \frac{Q}{2\sqrt{\pi Dt}}$$

$$\Rightarrow C(x,t) = \frac{Q}{\sqrt{2\pi(\Delta R_p^2 + 2Dt)}} \exp\left[-\frac{(x-R_p)^2}{2(\Delta R_p^2 + 2Dt)}\right]$$

Actual distributions





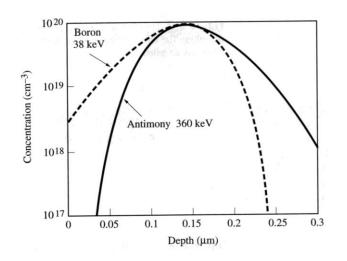
Simulated profiles of B implanted in amorphous or polycrystalline Si over a range of energies. Profiles show noticeable skew toward surface due to backscattering of light B ions. Skewed profiles are described by a 4-factor Pearson distribution. Gaussian fit to 500 keV profile is shown by dashed line.

Evolution of Gaussian profile after annealing. The Gaussian preserves its shape as it diffuses in an infinite medium. To accurately model, we need to include 4 parameters:

- 1. Projected range, R_p
- 2. Scatter, ΔR_p
- 3. "Skewness" due to mass effects, γ
- 4. Kurtusis, or variation due to occasional extremes, β

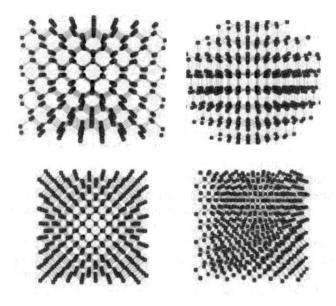
Thus

$$f(curve) = f(R_p, \Delta R_p, \gamma, \beta)$$

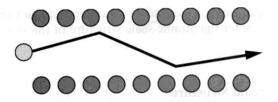


Simulation of ion-implanted B and Sb profiles with implant energy adjusted to give the same range. Light B ion skews toward surface, while heavy antimony ion skews toward bulk.

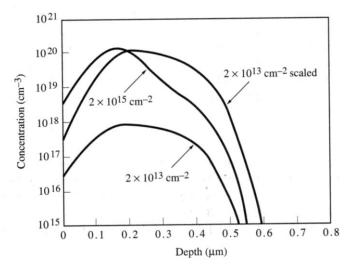
The role of crystal structure



Clockwise from top left: Image of a silicon crystal looking down the 110 axial channels, the 111 planar channels, the 100 axial channels, and with tilt and rotation to simulate a "random" direction



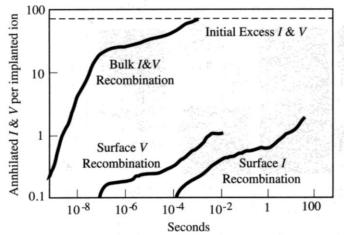
lons undergo many small angle deflections when traveling in a channel



Simulated boron profiles implanted into <100> Si wafers with zero tilt & rotation.

Implantation Damage & Annealing

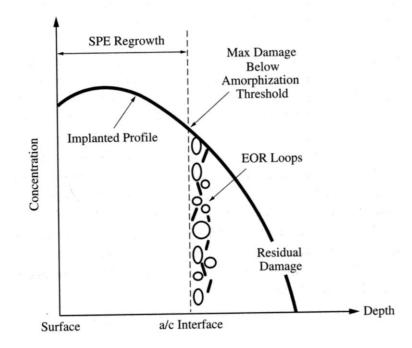
Goal: to remove damage from implantation and return Si to a crystalline state leaving dopants in substitutional sites.



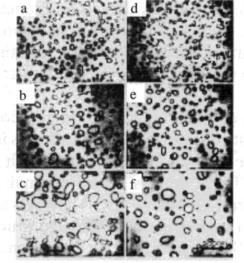
<311> Ribbom-like Defect Ribbom-like Defect <110> 0 I-dimer 311 Capture Radius

Simulation of recombination of vacancy (V) and interstitial (I) damage resulting from implantation. At 800 °C, recombination takes place very quickly. After a short time, only excess interstitial clusters remain

Si interstitials condense into ribbons of Si atoms (I-dimers) lying on {311} planes and grow by extending in the <110> directions. On right is a TEM image of clusters At high temperatures, ribbons condense and grow, creating dislocation loops that populate the boundary between crystalline and amorphous silicon.



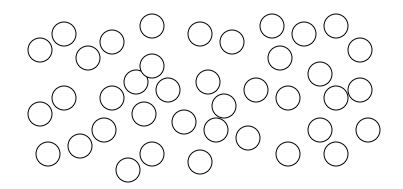
Schematic of stable End-of-Range (EOR) dislocation loops that form at amorphous/crystalline interface



TEM of EOR dislocation loops for annealing times of 5, 60 and 960 minutes at 850 oC (a-c), and for 1, 60 and 400 sec. at 1000 oC (d-f). Loops show some *ripening* (large loops grow at the expense of small loops) but are very stable at typical annealing temperatures.

Implantation Processes: Damage

- Ion collides with lattice atoms and knock them out of lattice grid
- Implant area on substrate becomes amorphous structure

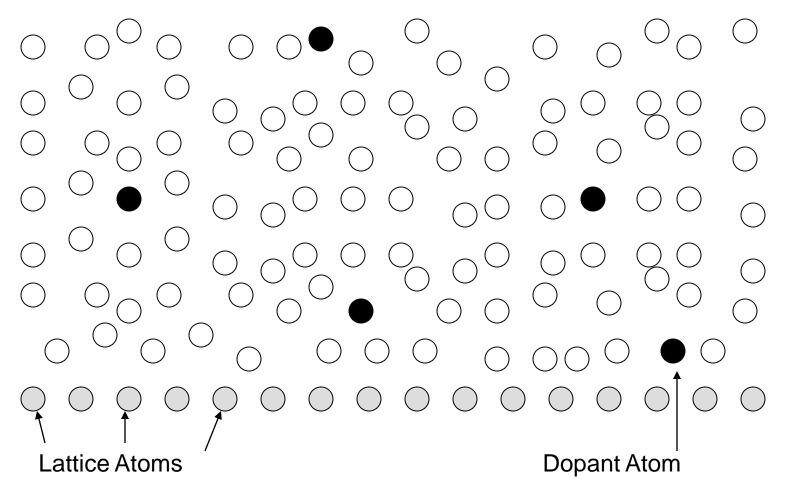


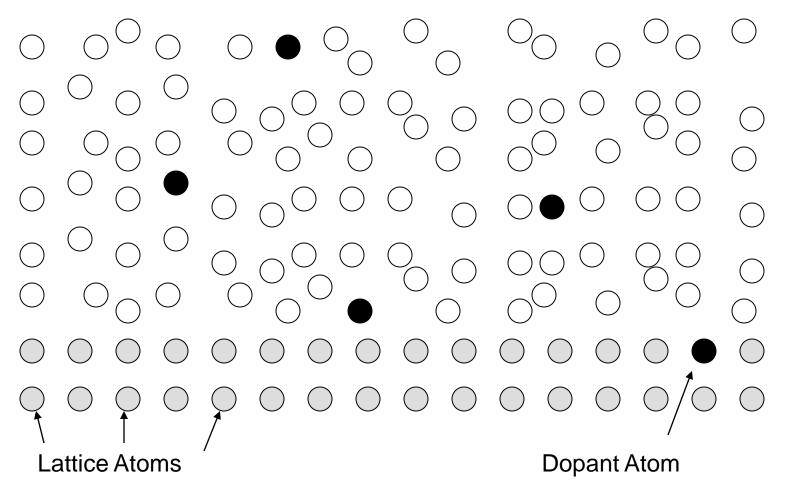
Before Implantation

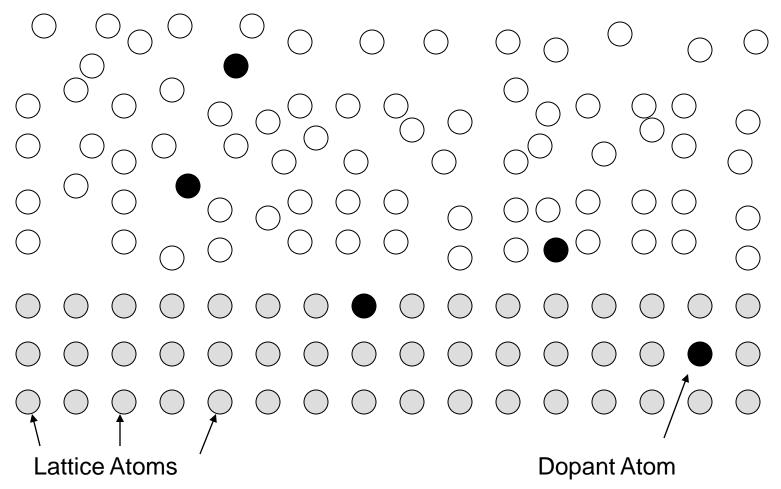
After Implantation

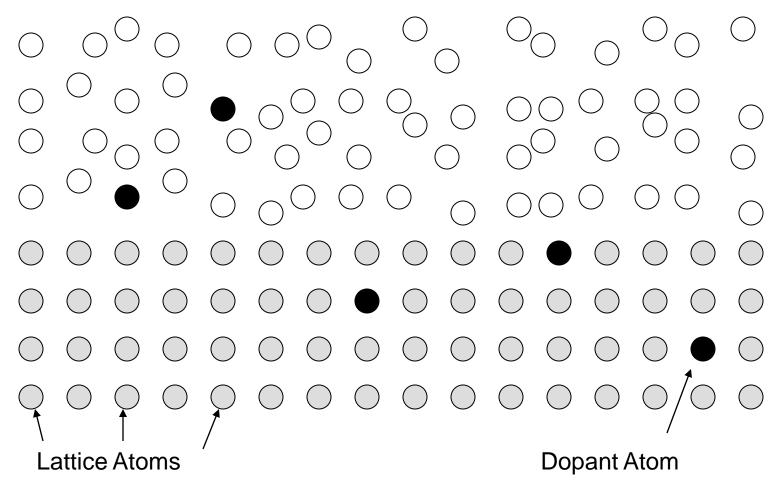
Implantation Processes: Anneal

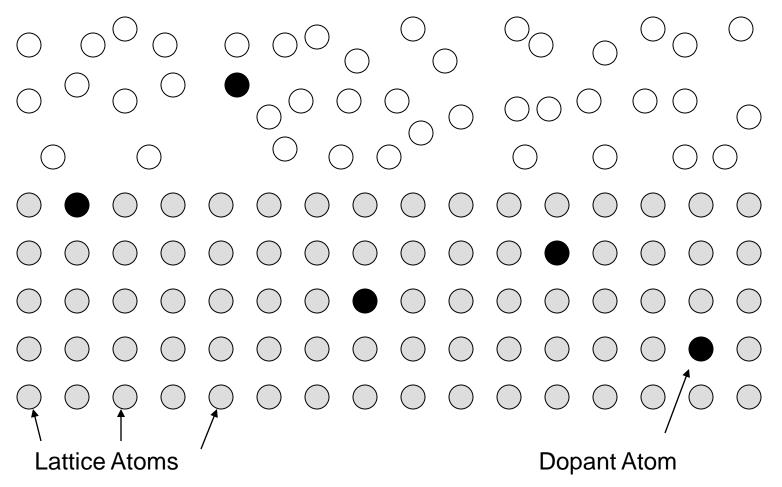
- Dopant atom must in single crystal structure and bond with four silicon atoms to be activated as donor (N-type) or acceptor (P-type)
- Thermal energy from high temperature helps amorphous atoms to recover single crystal structure.

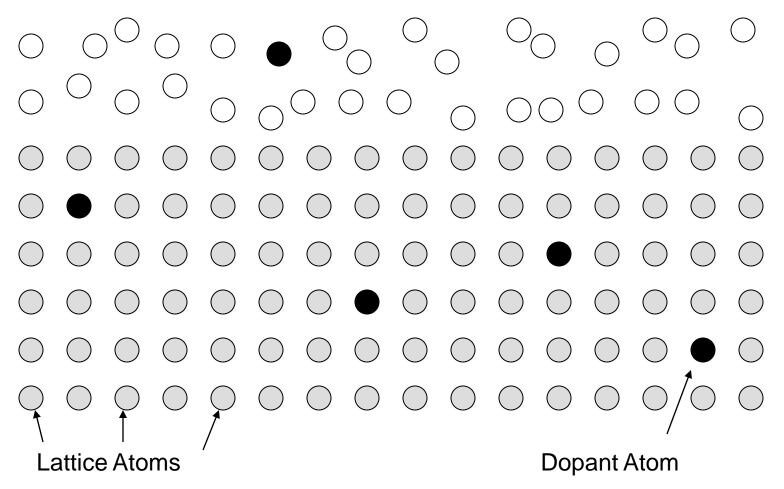


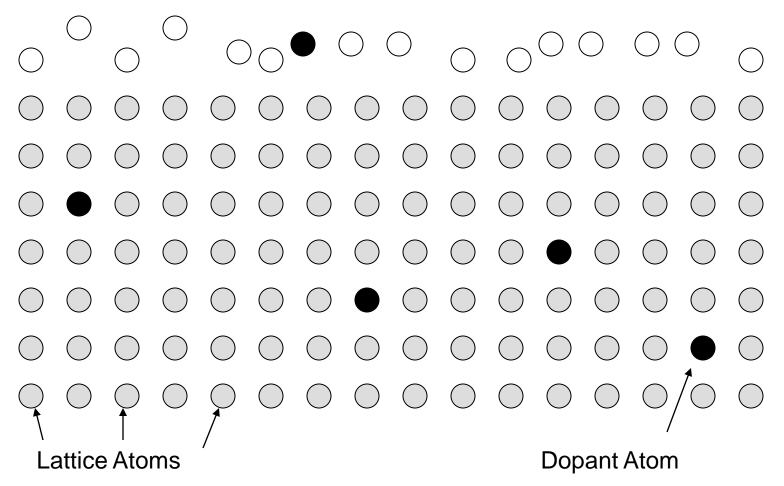


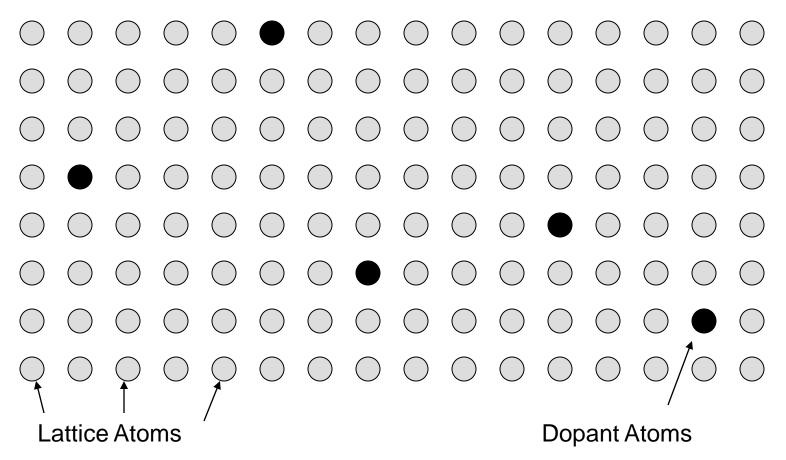




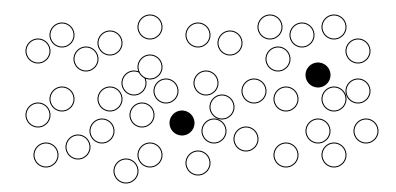








Implantation Processes: Annealing



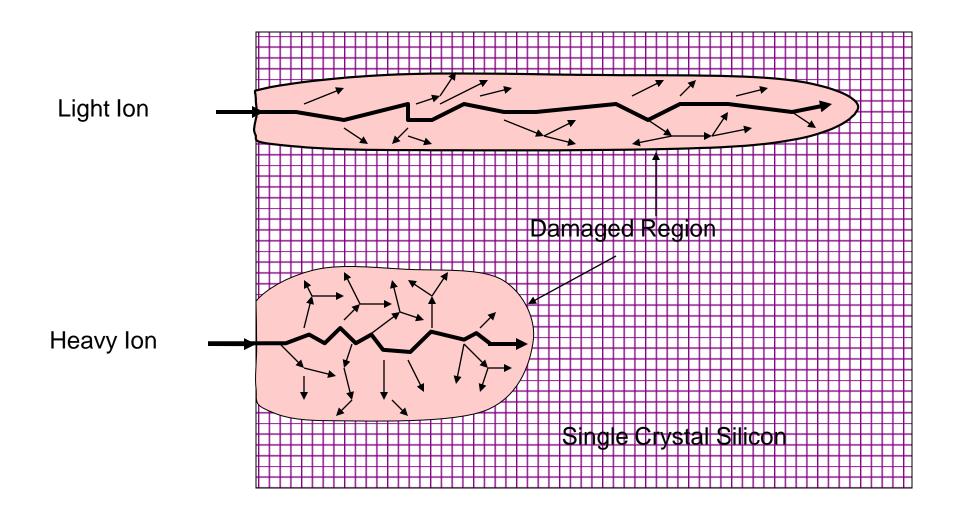
Before Annealing

After Annealing

Rapid Thermal Annealing (RTA)

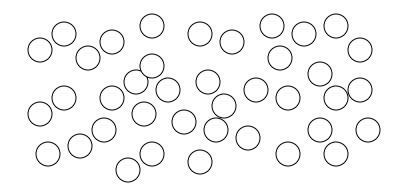
- At high temperature, annealing out pace diffusion
- Rapid thermal process (RTP) is widely used for post-implantation anneal
- RTA is fast (less than a minute), better WTW uniformity, better thermal budget control, and minimized the dopant diffusion

Lattice Damage With One Ion



Implantation Processes: Damage

- Ion collides with lattice atoms and knock them out of lattice grid
- Implant area on substrate becomes amorphous structure



Before Implantation

After Implantation

Monte Carlo Simulation of 50keV Boron implanted into Si

